

$P_{in}$  (Input-Signal)



$P_{clk}$  (Clock-Signal)



$P_{reg}$  (Regenerated-Signal)



Figure 1

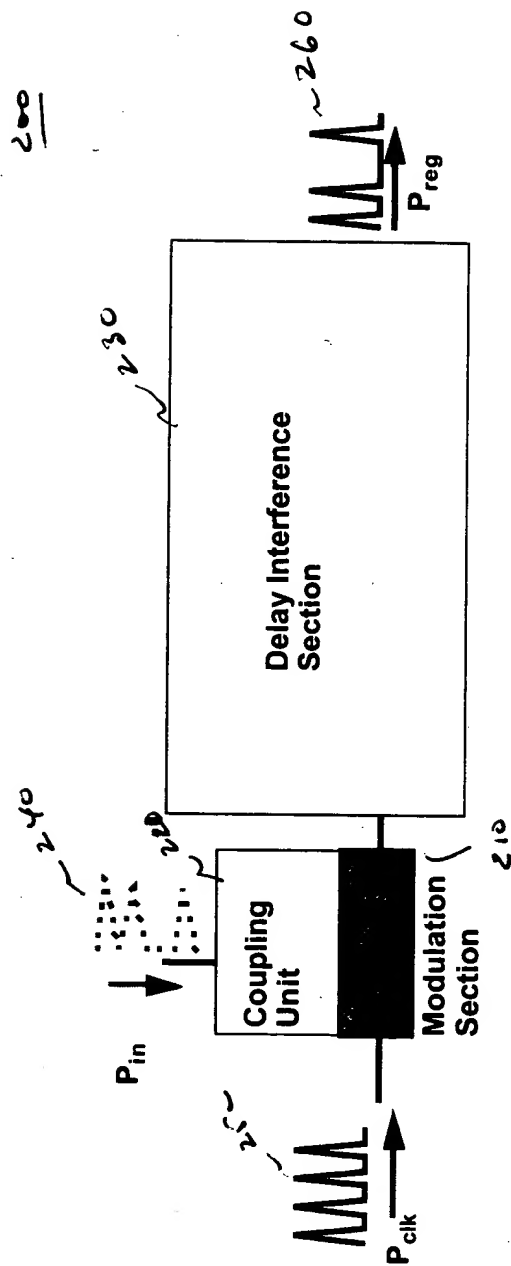


FIGURE 2

09745765-12200  
002221-58/54260

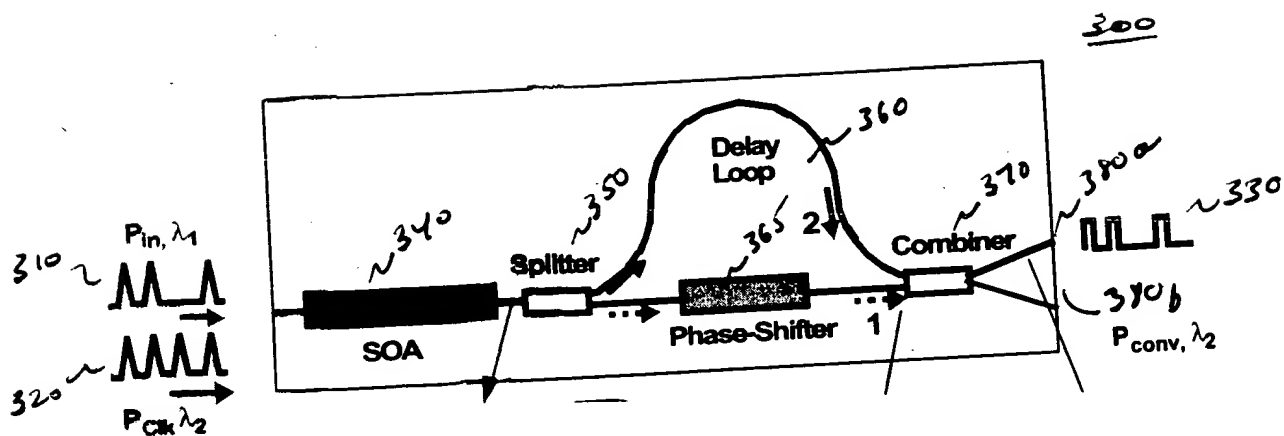


Figure 3

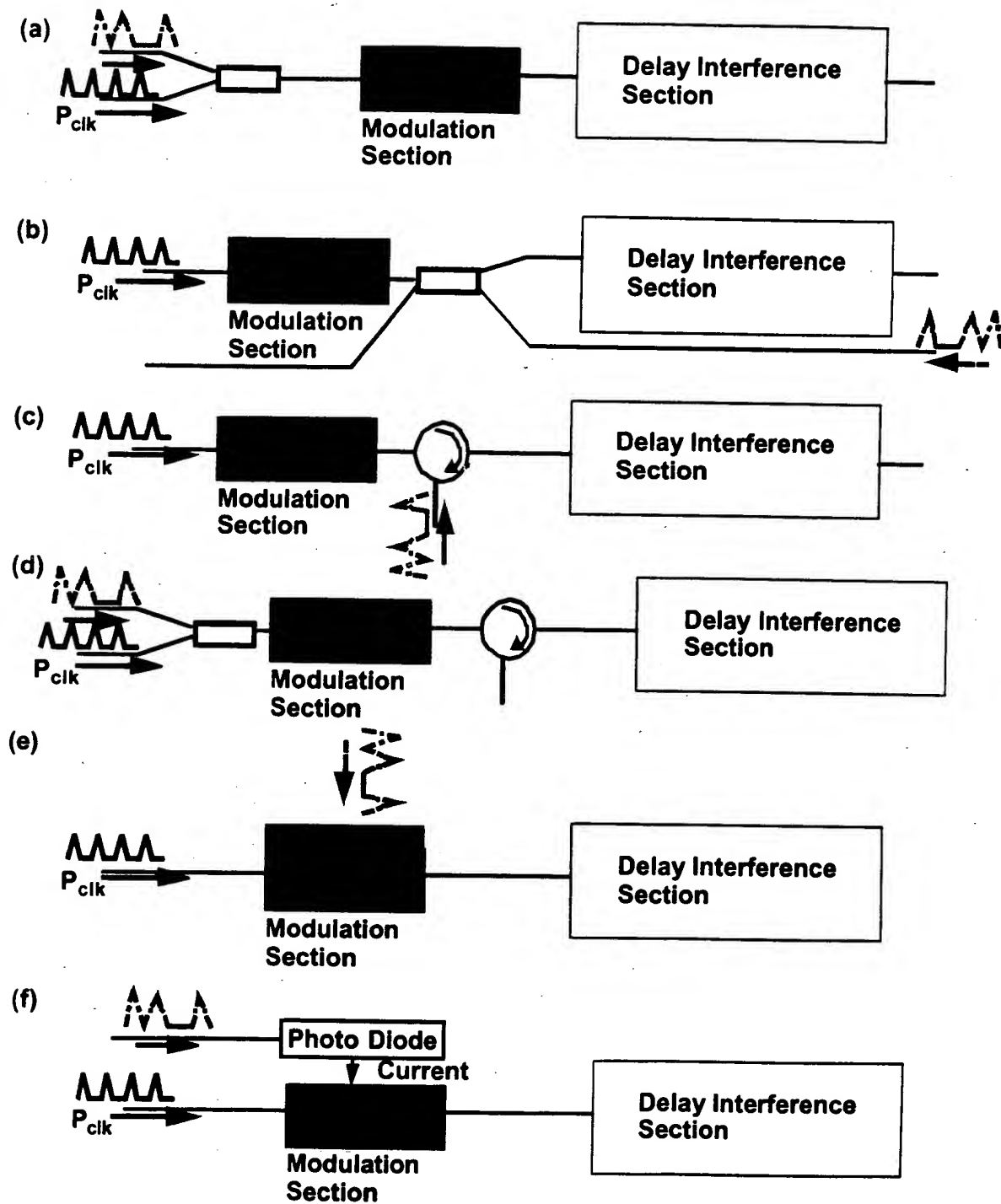
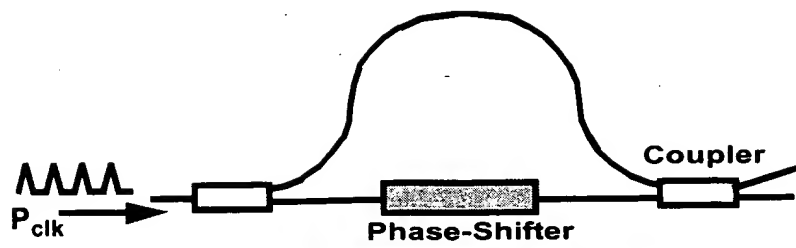


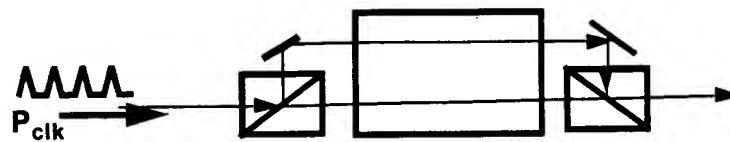
Figure 4

002227 5875460

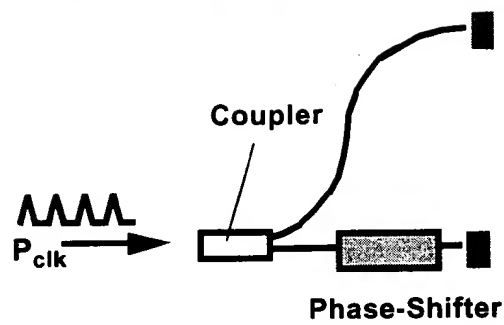
(a<sub>1</sub>)



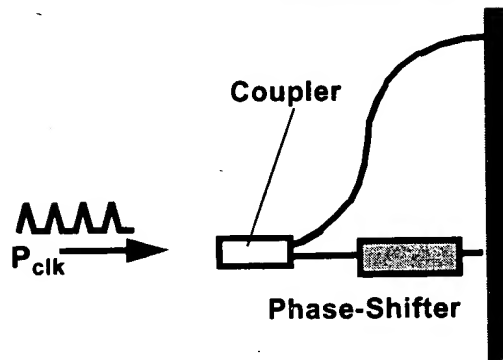
(a<sub>2</sub>)



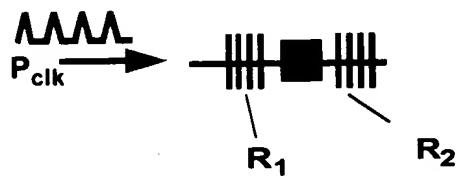
(b)



(c)



(d)



(e)

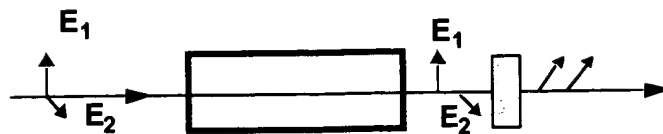


Figure 5

09745785.12200

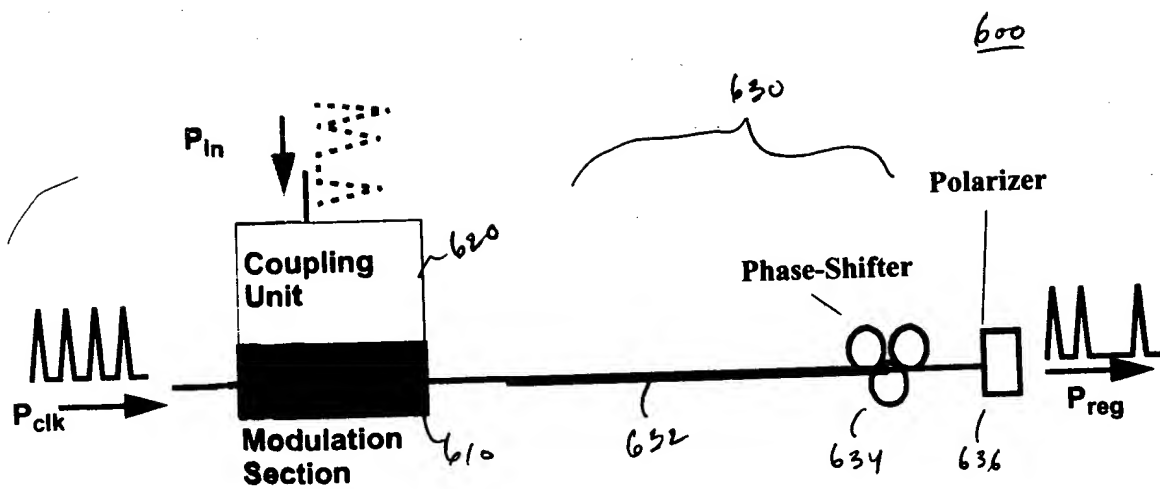


Figure 6(a)

09745785-12200

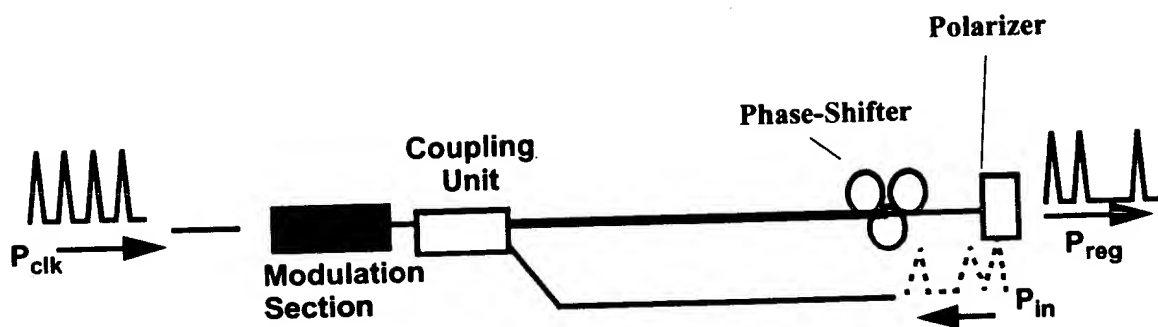


Figure 6(b)